Today

3:48am Searched for shared cache cmp

2:57am Searched for Prefetching in an Intelligent Memory Architecture Using a Helper Thread - Viewed 2 results
- CiteSeerX — Prefetching in an Intelligent Memory Architectu... - psu.edu
- CiteSeerX — Prefetching in an Intelligent Memory Architectu... - psu.edu

2:52am Searched for pushing data into a cache cmp - Viewed 1 result
- Protocol for maintaining cache coherency in a CMP - Patent ... - freepatentsonline.com

2:43am Searched for site:ieeexplore.ieee.org speculative precomputation on chip multiprocessors ieee

2:43am Searched for speculative precomputation on chip multiprocessors ieee - Viewed 2 results
- Mitosis: A Speculative Multithreaded Processor Based on ... - ieee.org
- http://scholar.google.com/scholar?hl=en&q=author:%22Brown%22+...

2:43am Searched for speculative precomputation on chip multiprocessors - Viewed 2 results
- http://www.cs.uwaterloo.ca/~brecht/courses/856/Possible-Readi...
- Accelerating sequential programs on Chip Multiprocessors via ... - acm.org

2:32am Searched for slipstream execution mode for cmp-based multiprocessors - Viewed 1 result
- Slipstream Execution Mode for CMP-Based Multiprocessors - ncsu.edu